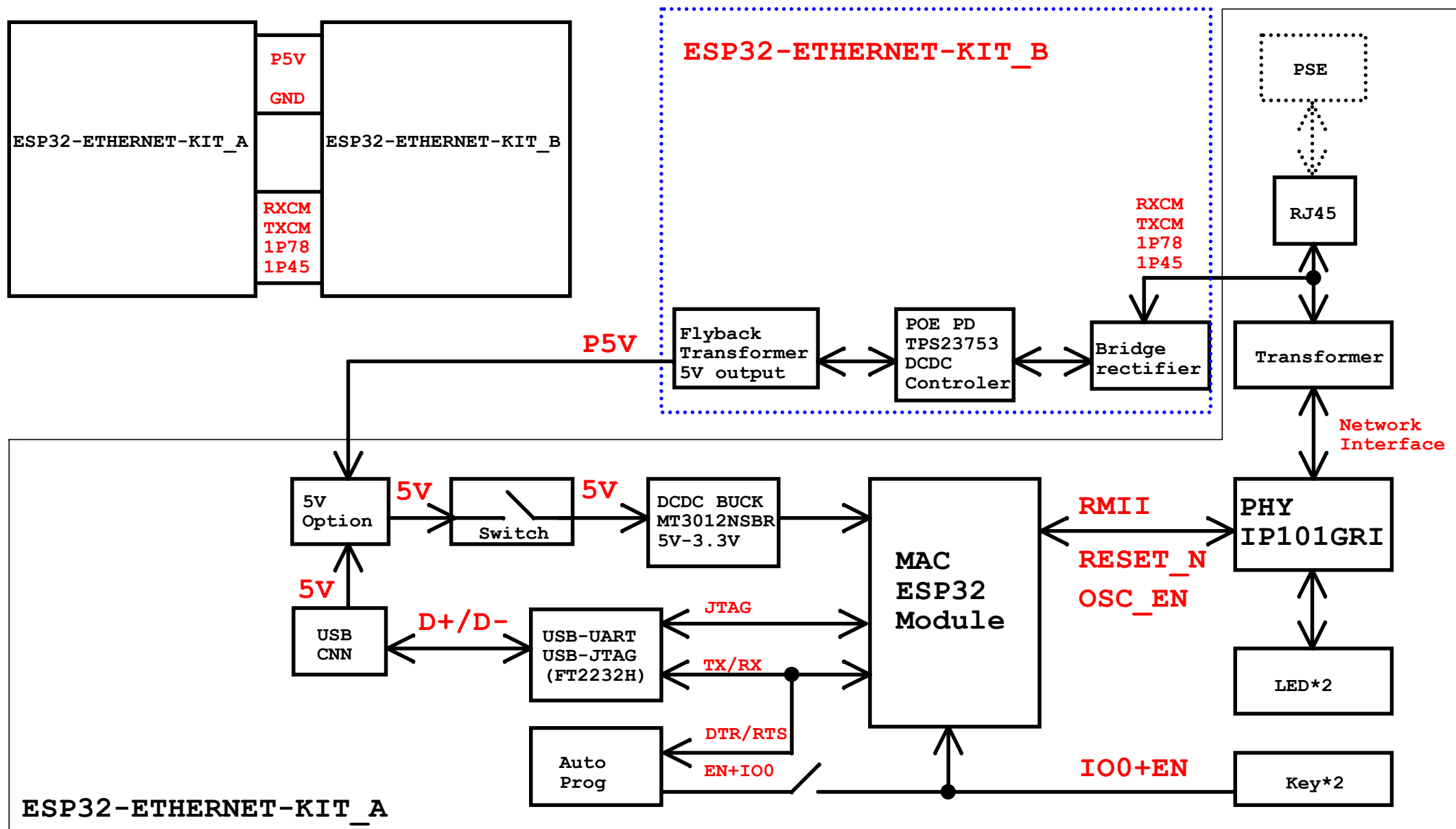


System Block:



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Title
<ESP32-Ethernet-Kit_A>

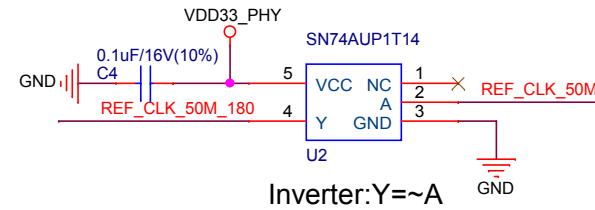
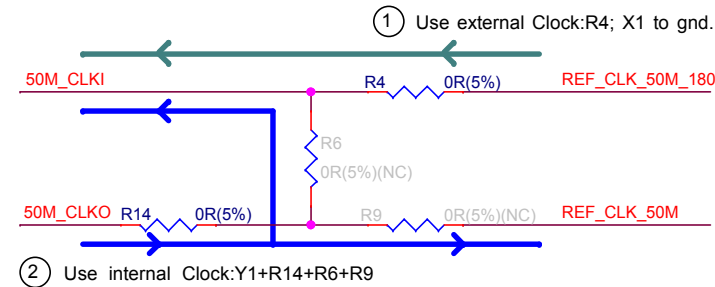
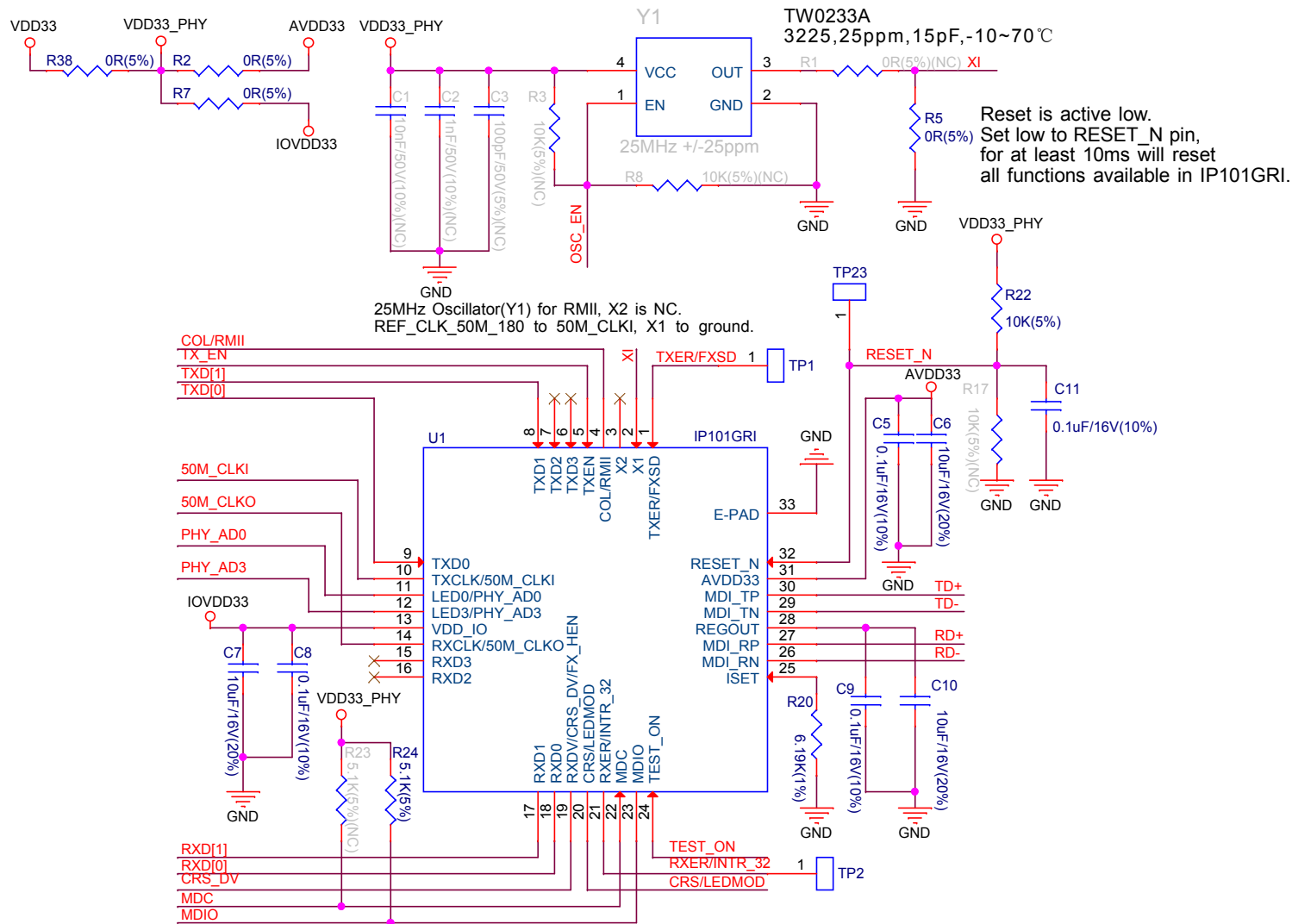
Size
A3

Document Number
<01 : DesignBlock>

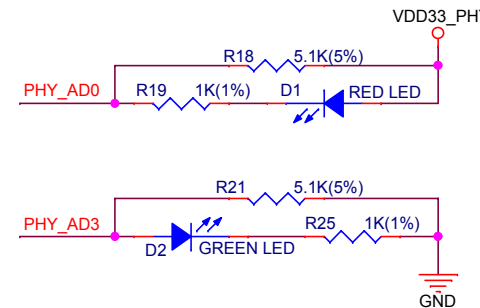
Date: Friday, May 17, 2019

Sheet 1 of 4

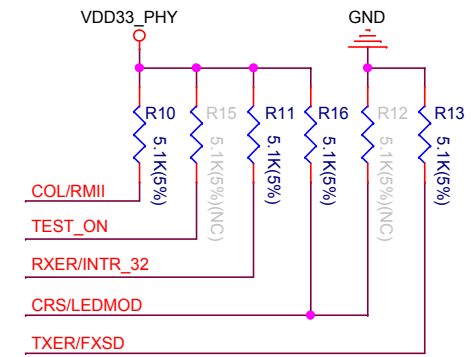
Rev
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DEFAULT : 0XX1



Mode Configuration:



COL/RMII
0: MII Mode
1: RMII Mode
Default: RMII Mode

TEST_ON
1: Enable Test mode.
NC: Normal operation
Default: Normal operation

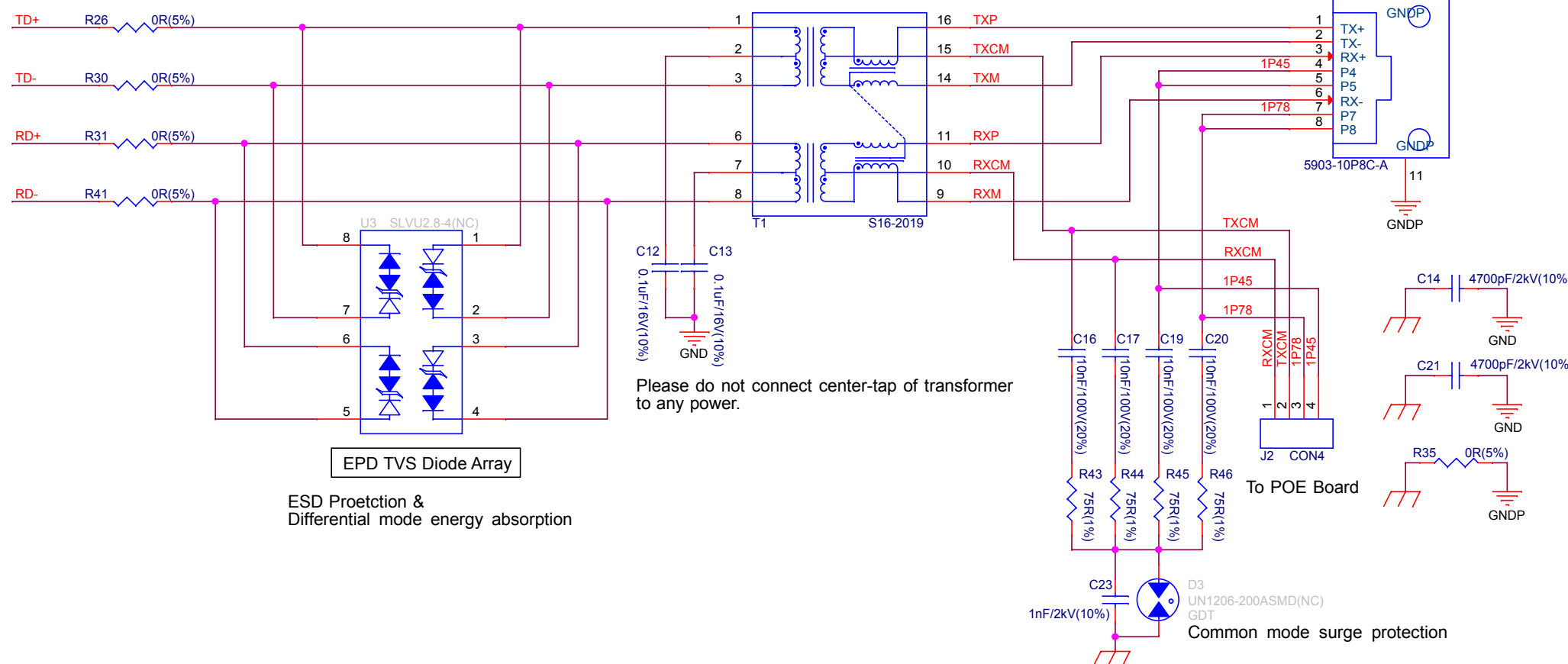
RXER/INTR32
1: INTR funtion
0: RXER funtion
Default: INTR funtion

CRS/LEDMOD
1: LED mode 2
0: LED mode 1
Default: LED mode 2

TXER/FXSD
1: FXSD
0: TXER
Default: TXER

PHY

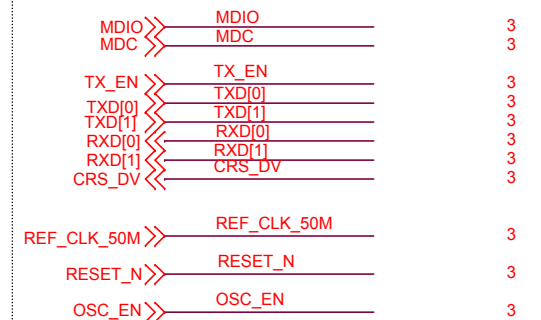
Built-in 49.9ohm resistors



Serial Management Interface:
MDIO:GPIO18
MDC:GPIO23

RMII:
TX_EN:GPIO21
TXD[0]:GPIO19
TXD[1]:GPIO22
RXD[0]:GPIO25
RXD[1]:GPIO26
CRS_DV:GPIO27

REF_CLK_50M:GPIO0
RESET_N:GPIO5
OSC_EN:GPIO2



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Document Number
<02 : PHY_IP101GRI>

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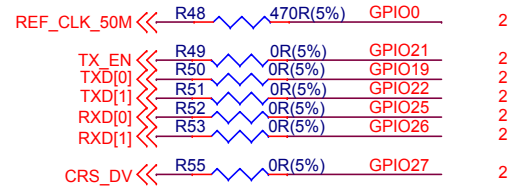
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RMII:
CLK_OUT:GPIO0

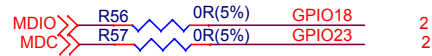
TX_EN:GPIO21
TXD[0]:GPIO19
TXD[1]:GPIO22

RXD[0]:GPIO25
RXD[1]:GPIO26

CRS_DV:GPIO27



Serial Management Interface:
MDIO:GPIO18
MDC:GPIO23



RESET_N:GPIO5



OSC_EN:GPIO2



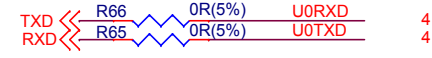
JTAG:

MTCK:GPIO13
MTDI:GPIO12
MTDO:GPIO15
MTMS:GPIO14
nTRST:GPIO4



UART:

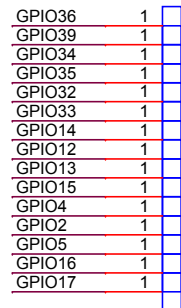
U0TXD
U0RXD



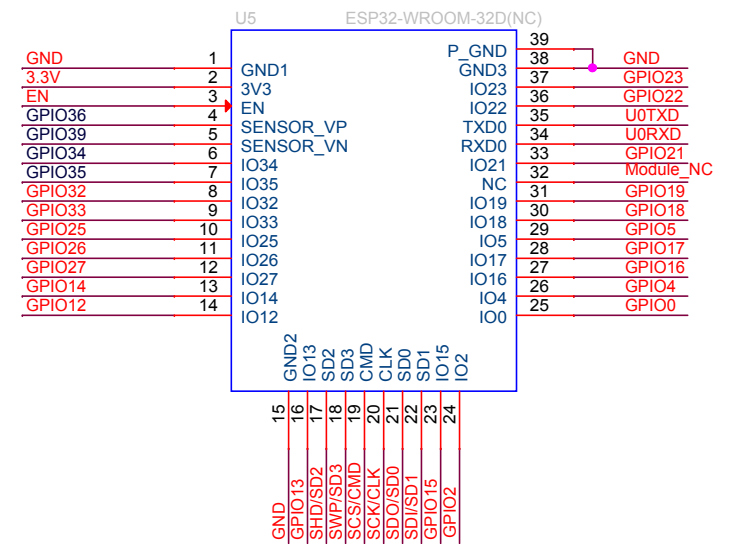
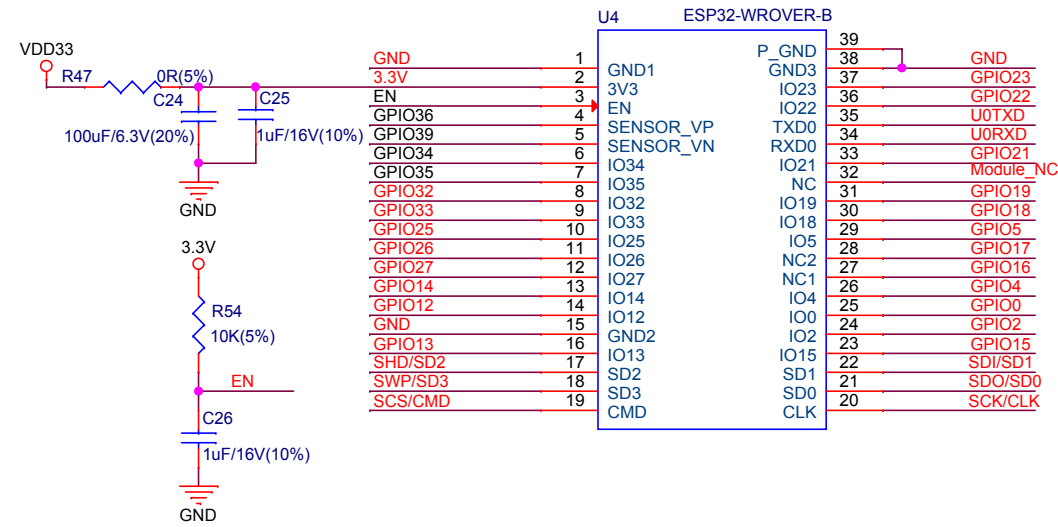
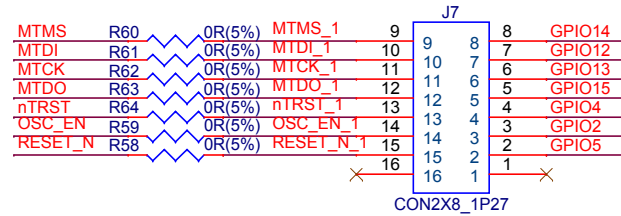
Auto-Prog:



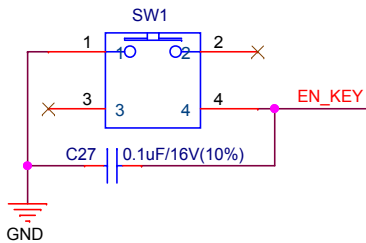
GPIO:



ON ← OFF

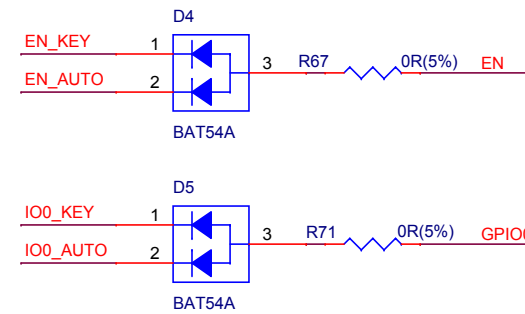


RESET:

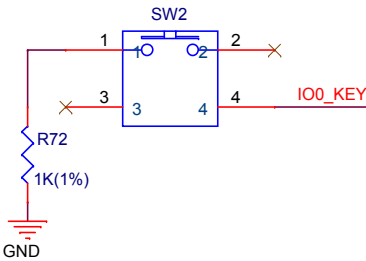


UART DOWNLOAD:
GPIO0->0,GPIO2->0

FLASH BOOT:
GPIO0->1



BOOT MODE:



STRAP PINS:



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<ESP32-Ethernet-Kit_A>

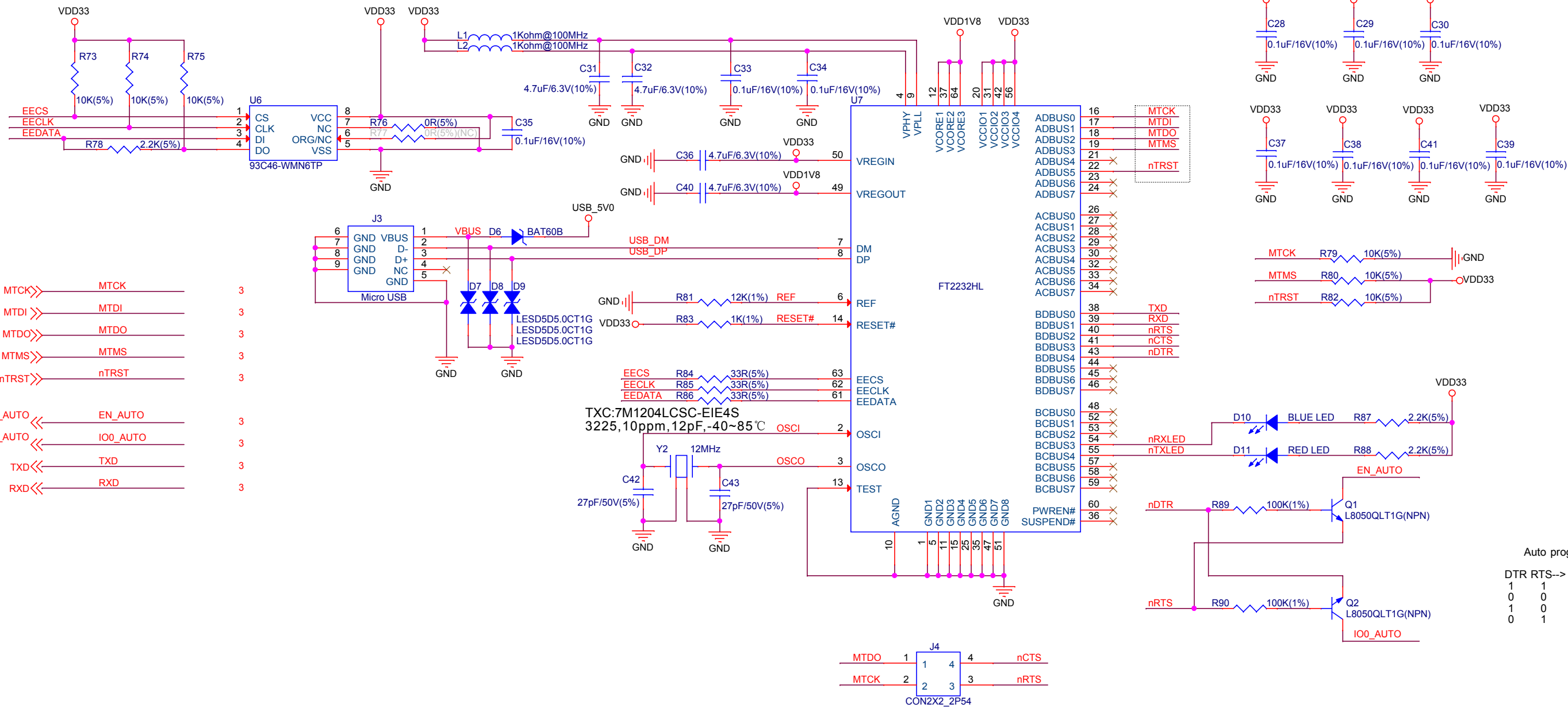
Size Document Number

A3 <03 : MAC_ESP32>

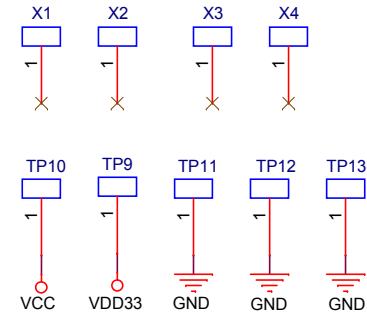
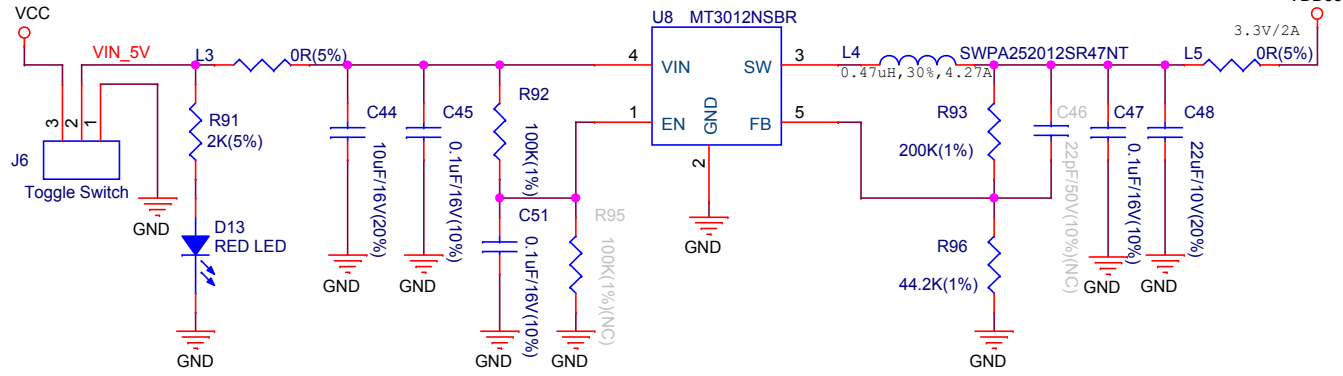
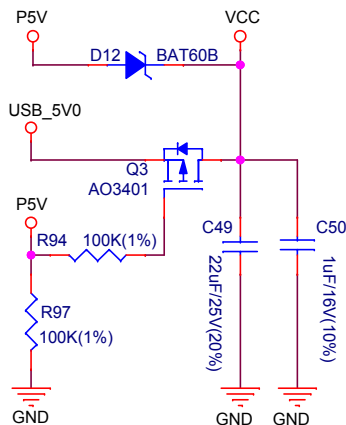
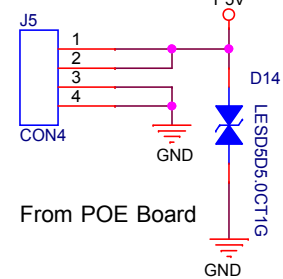
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External 5V Power



1. If POE board is plugged in, VCC will be only powered by POE.
2. Otherwise, VCC is powered by USB.

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Title <ESP32-Ethernet-Kit_A>

Size Document Number A3 <04 : UART&JTAG Debug>

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