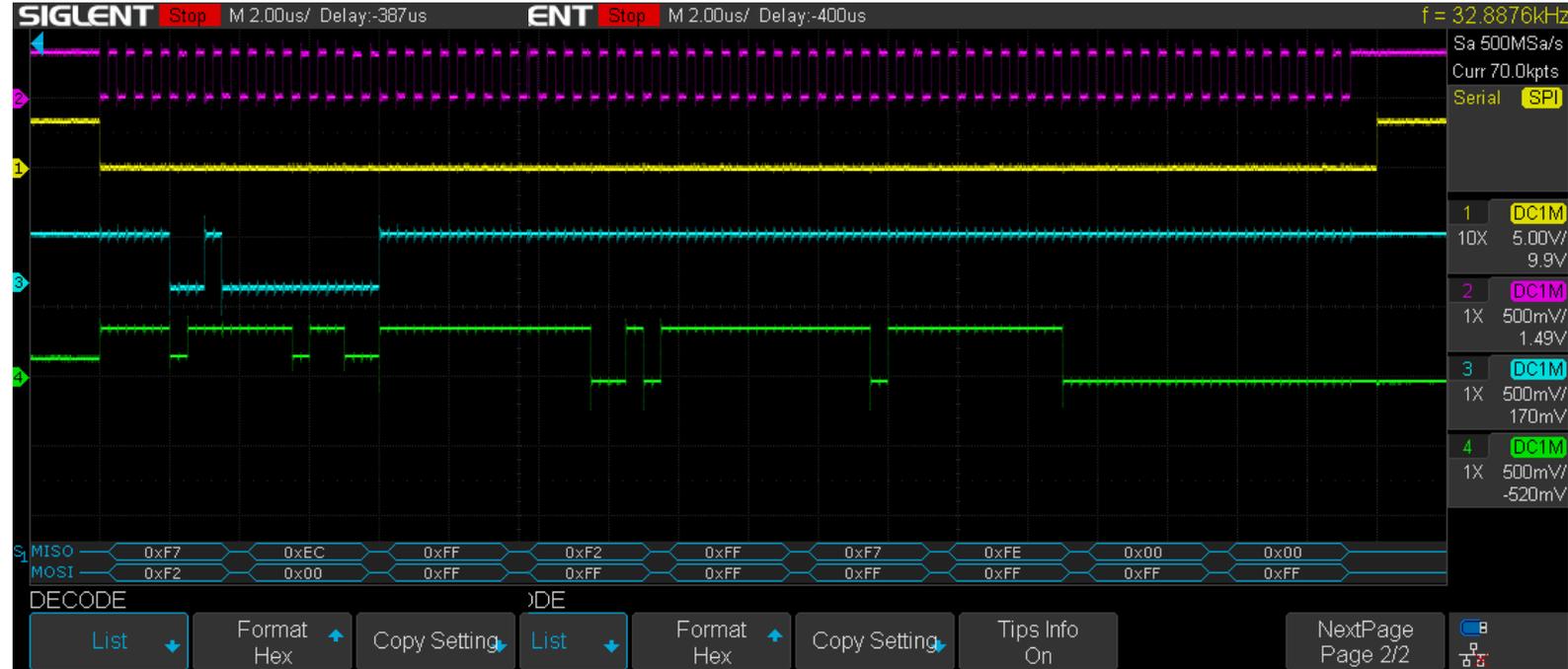


# Multi-byte read of ADXL345 on ESP32S2 -- upright



- Board upright, Read, MB, Register 0x32... 0xF2 is the command
- 2uS/div... clock is 2MHz
- 9 bytes transferred (1<sup>st</sup> byte is command, returning garbage)
  - Asked for 6 bytes data (8 bytes transferred). Perhaps extra byte is to make data phase multiple of 4 bytes?
  - Ordering the bytes correctly, x: 2, y: -14, z: 229 (close to 1g)

# Multi-byte read of ADXL345 on ESP32S2 – upside down



- Board upside down, Read, MB, Register 0x32... 0xF2 is the command
- 2uS/div... clock is 2MHz
- 9 bytes transferred (1<sup>st</sup> byte is command, returning garbage)
  - Asked for 6 bytes data (8 bytes transferred). Perhaps extra byte is to make data phase multiple of 4 bytes?
  - Ordering the bytes correctly, x: -20, y: -14, z: -265 (close to -1g)